PATENT NUMBER and ISSUE DATE

U.S. UTILITY Patent Application

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**CONTINUING DATA VERIFIED: (1930-22-27)										
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35 USC 119 cor Verified and Ad TITLE: Form board solder	knowledged Examiners'	s intials	•	circuit boa		D-131 p chip joints and board pt. OF COMM/PAT.8 TM-PTO-438				

NOTICE OF ALLOWANCE MAILED			MS ALL	LLOWED					
		Assistant Examiner	Total Claims		Print Claim for O.G				
ISSUE FEE			DRAWING						
		4	Sheets Drwg. Figs.D		wg.	Print Fig.			
Amount Due	Date Paid	į .			-	ł			
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		Primary Examiner	1						
TERMINAL			Application Examiner						
		PREPARED FOR ISSUE							
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